

10191 1614

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : SPITZ et al.
International Application No. : PCT DE00/01295
International Filing Date : April 26, 2000
U.S. Serial No. : 09/720,720
For : **METHOD FOR ELIMINATING DEFECTS IN
SILICON ELEMENTS THROUGH
SELECTIVE ETCHING**

Assistant Commissioner for Patents
Box PCT
Washington, D.C. 20231

Attention: DO/EO/US

**RESPONSE TO MISSING REQUIREMENTS
UNDER 35 U.S.C. 371**

SIR:

In response to the Notification of Missing Requirements Under 35 U.S.C. 371 in the United States Designated/Elected Office (DO/EO/US) (mailed January 30, 2001), Applicants submit herewith a fully executed Declaration, in order to complete the filing requirements for the U.S. national phase of the above-identified PCT application. The application filed in the Patent Office is the application which the inventors executed by signing the Declaration and Power of Attorney. A copy of the Notification of Missing Requirements is also enclosed.

The Office is authorized to charge the \$130.00 fee to cover the surcharge for late filing of the Declaration.

The Office is also hereby authorized to charge Deposit Account No. 11-0600 with any additional fees required by this paper or credit any overpayment. An additional copy of this letter is enclosed for this purpose.

Respectfully submitted,
KENYON & KENYON



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Date: 2/28/01

JG09 Rec'd PCT/PTO 28 FEB 2001

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